

Hardware Asset Management (HWAM) Capability Defect False Positives Guide

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Defect False Positive Condition Descriptions

Condition	Description
Desired State Process	A defect false positive condition that occurs when the desired state data is not synchronized, inaccurate, or incomplete. This condition is often the result of manual processes.
Architecture and Sensor Deployment	<p>A defect false positive condition that occurs when an actual state sensor is unable to collect necessary data or communicate with the actual state collection manager (ASCM) due to a poor design, network configurations, sensor collection/reporting limitations, or sensor failures.</p> <p>A defect false positive condition that occurs when the collection system is not updated with the collected and reported data due to poor design, configuration issues, or collector failures.</p>
Data Integrity	A defect false positive condition that occurs when tools and systems providing the actual state data have inconsistencies in how they collect, report, and store the data. This condition also occurs if there is a failure in the data storage mechanism.

HWAM Defects

#	Description
HWAM-1	A device is in the actual state inventory, but NOT in the authorized hardware inventory.
HWAM-2	A device is in the authorized hardware inventory and the actual inventory, but no one is assigned to manage it.
HWAM-3	A device is in the authorized hardware inventory, but not in the actual inventory. The status in the authorized hardware inventory does not provide a reason for not being in the actual inventory.
HWAM-4	A device is in both the authorized hardware and actual inventories, but the authorization is expired.

HWAM Defect Mapping to False Positive Conditions

	Desired State Process	Architecture and Sensor Deployment	Data Integrity
HWAM-1	X		X
HWAM-2	X		
HWAM-3	X	X	X
HWAM-4	X		